

Shop # _____ Customer Name : _____ Catalog # _____
 Tech's initials : _____ Date : _____ Serial # _____

Done	N/A	Bulletin	Description	Part #	Description
<input type="checkbox"/>	<input type="checkbox"/>	II:1	Correct floppy drive termination.	WA-0373	4 wire jumper
<input type="checkbox"/>	<input type="checkbox"/>	II:2	Proper 32K/64K RAM board jumper configurations.		
<input type="checkbox"/>	<input type="checkbox"/>	II:4	Correct thermal failure of FDC IC by using Zener Diode.	DX-2914	2.7V zener 1N5223A
<input type="checkbox"/>	<input type="checkbox"/>	II:5	Correct failure of Motorola video board causing VDG damage.	470 ohm 1/4W res.	
				10K ohm 1/4W res.	
				1.5K ohm 1/4W res.	
<input type="checkbox"/>	<input type="checkbox"/>	II:9	Satellite modification for early FDC boards.	AXX-0505	Satellite PCB
<input type="checkbox"/>	<input type="checkbox"/>	II:10	FDC modification to prevent Xenix from seeing double-sided disks when a terminator is used.		
<input type="checkbox"/>	<input type="checkbox"/>	II:13	To correct reversed C17 in FDC boards.		
<input type="checkbox"/>	<input type="checkbox"/>	II:14	Correct retrace problem with RCA video board.	DX-1168	Fast recovery diode
<input type="checkbox"/>	<input type="checkbox"/>	II:15	Explain proper tightening of Shugart stepper motor bracket.		
<input type="checkbox"/>	<input type="checkbox"/>	II:16	Correct "Boot Error DC" after running Scripsit on Shugart.	.47xMF	35V elec. cap
<input type="checkbox"/>	<input type="checkbox"/>	II:18	Correct dual addressing on VDG (Rev. A and B).		
<input type="checkbox"/>	<input type="checkbox"/>	II:19	Correct jumpering for late style FDC in Model II.		
<input type="checkbox"/>	<input type="checkbox"/>	II:20	Mod. to discrete Shugart drive to correct boot errors.	.01xMF	ceramic cap.
<input type="checkbox"/>	<input type="checkbox"/>	II:21	Mod. to early FDC to allow format beyond track 43.		
<input type="checkbox"/>	<input type="checkbox"/>	II:22	Proper sector sensor adjustment on Shugart drives.		
<input type="checkbox"/>	<input type="checkbox"/>	II:23	Mod. to VDG to correct unexplained keyboard entries and machine lockup.		
<input type="checkbox"/>	<input type="checkbox"/>	II:24	Mod. to VDG to correct unexplained characters on screen.		
<input type="checkbox"/>	<input type="checkbox"/>	II:25	Mod. to improve power on reset circuit.	51K ohm	res.
<input type="checkbox"/>	<input type="checkbox"/>	II:26	Enhanced DMA modification instructions to CPU board.		
<input type="checkbox"/>	<input type="checkbox"/>	II:27	Insure side select signals go to and from external drive connector.		
<input type="checkbox"/>	<input type="checkbox"/>	II:28	Correct head load termination on FDC board.		
<input type="checkbox"/>	<input type="checkbox"/>	II:29	Astec AA11081 power supply adjustment procedure.	AXX-6008	Power Supply
				*AW-2841	Wire harness
<input type="checkbox"/>	<input type="checkbox"/>	II:30	Termination/jumpering/test points for Shugart, CDC and TPI.		
<input type="checkbox"/>	<input type="checkbox"/>	II:31	Mod. to memory boards to prevent memory errors.		
<input type="checkbox"/>	<input type="checkbox"/>	II:32	Insure CPU board is correctly jumpered for wait states.		
<input type="checkbox"/>	<input type="checkbox"/>	II:33	CHECKSUM's for Model II/16 BOOT ROM's.		
<input type="checkbox"/>	<input type="checkbox"/>	II:35	Insure termination is properly wired on Shugart drives.		

NOTE: * Indicates that these Parts may not be readily available,
 contact Technical Support for assistance.

Done	N/A	Bulletin Description	Part # / Description
<input type="checkbox"/>	<input type="checkbox"/>	II:37 Mod. to Motorola Video board when using modified power supply.	
<input type="checkbox"/>	<input type="checkbox"/>	II:38 Check for shorted Z4 on replacement Keytronics keyboards.	
<input type="checkbox"/>	<input type="checkbox"/>	II:39 Correct TPI drives which errors as Drive 0 and does not function in the expansion bay.	
<input type="checkbox"/>	<input type="checkbox"/>	II:41 Mod. to correct keyboard power on reset problem.	0.33xMF ceramic cap.
<input type="checkbox"/>	<input type="checkbox"/>	16:1 Jumpering configuration, and proper cabling when performing FDC alignments.	
<input type="checkbox"/>	<input type="checkbox"/>	16:3 Mod. to correct intermittent operation of the CPU board.	MX-5709 74S74
<input type="checkbox"/>	<input type="checkbox"/>	16:5 Correct backwards installation of C41 on Rev.D Z80 CPU brds.	0.33xMF 50V axial elec. cap.
<input type="checkbox"/>	<input type="checkbox"/>	16:6 Correct "Error 8" and other problems on Drive 1.	
<input type="checkbox"/>	<input type="checkbox"/>	16:7 Proper adjustment of head load arm and disk eject trigger.	
<input type="checkbox"/>	<input type="checkbox"/>	16:8 Modification to motor speed circuit.	20K 1/2W 5% res. 0.47xMF 50V cap. 33xMF 6.3V cap.
<input type="checkbox"/>	<input type="checkbox"/>	16:9 Proper termination for Thinline drives.	
<input type="checkbox"/>	<input type="checkbox"/>	16:10 Insure a 2 Amp, slow blow fuse on 24V rail for AA11092 P.S.	2.0 Amp slow blow fuse
<input type="checkbox"/>	<input type="checkbox"/>	16:13 Mod. to correct intermittent speed errors.	
<input type="checkbox"/>	<input type="checkbox"/>	16:14 Mod. to correct early Tandon Thinline drive problems.	HD-0639 Screw and washer
<input type="checkbox"/>	<input type="checkbox"/>	16:15 Procedure to raise +5V rail on power supply.	47K ohm 1/4W res.
<input type="checkbox"/>	<input type="checkbox"/>	**16:18 Mod. to eliminate memory buffer problems.	
<input type="checkbox"/>	<input type="checkbox"/>	16:19 Mod. to insure good grounds between 68K CPU and memory brds.	
<input type="checkbox"/>	<input type="checkbox"/>	16:20 Mod. to correct data setup time to memory board during write cycle.	
<input type="checkbox"/>	<input type="checkbox"/>	16:25 Modification to allow use of the Disk Cartridge System.	MB-0038 bracket extender
<input type="checkbox"/>	<input type="checkbox"/>	6000:2 Mod. to correct timing error on 68K CPU bd.	
<input type="checkbox"/>	<input type="checkbox"/>	6000:4 Insure compatibility of inverters in video board.	MX-2801 74LS04
<input type="checkbox"/>	<input type="checkbox"/>	**6000:6 Reliability mods for 68K CPU bd.	MX-6579 74AS373
			MX-2119 (under cat# 25-5000)
			MX-0780 74LS138
			MX-5735 (under cat# 25-3020)
			330 pF ceramic cap.
			MXP-0032 U48 PAL
			MXP-0009 (under cat# 26-6014)

NOTE: ** Indicates Mandatory Modification

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[_]	[_]**6000:8	Reliability mods. for 68K mem. bd.	MX-2119	74F240
			MX-5853	74ALS244
			MX-6120	(under cat# 26-4002)
				100K ohm 1/4W 5% res.
				1xMF, 16V+ elec. cap.
				330pF ceramic cap.
[_]	[_]**6000:9	Reliability mods. to video board	MX-3864	(under cat# 25-1000)
			MX-4225	(under cat# 25-3000)
			MX-2801	74LS04
[_]	[_]	6000:12 Correct installation of R87 on belt drive Tandon logic brds.		
[_]	[_]**6000:13	Prevent "BUGCHK:SCSIFI" errors in Xenix.		
[_]	[_]	12/16B:12 Mod. to slow video RAM access which improves reliability.		
[_]	[_]	12/16B:19 Insure correct switch settings for 68K RAM bds.		
[_]	[_]**12/16B:24	Memory buffer modification.	MX-5703	MC3482AL
			MX-5704	MC3482BL
			MX-6553	74S373
			MX-6579	74AS373
[_]	[_]	12/16B:26 Mod. to keep power supply from going into current limiting mode.		10 ohm 1/4W res.
				0.33 ohm 2W res.
[_]	[_]	12/16B:31 Mod. to improve VDG operation.		
[_]	[_]	12/16B:34 Defective decoding chips causing Xenix errors.	MX-5849	(under cat# 26-1130)
[_]	[_]	12/16B:38 8 MHz upgrade procedure.		
[_]	[_]**12/16B:41	Reliability modifications.	MXP-0032	U48 PAL
				330 pF ceramic cap.
[_]	[_]	12/16B:44 Mod. to allow Xenix 3.x to run on a 6 MHz CPU board.	MXP-0032	U48 PAL
			MXP-0009	U36 PAL
			CF-1830	330pF cap.
[_]	[_]	INF:14 Correct failure to regulate due to damaged capacitor.		220xMF 16V elect. cap.

NOTE: ** Indicates Mandatory Modification

Done	N/A	Bulletin Description	Part # / Description
[_]	[_]	HD:1	Insure correcting jumpering on 8 Meg Interface.
[_]	[_]	**HD:12	Insure proper termination and level of HD interface signals. 470 ohm 1/4w res.
[_]	[_]	**HD:18	Mod. to prevent false triggering of the CTC when connected to the 12 Meg interface.
[_]	[_]	HD:28	Reduce occurrence of "Active Drive not ready" in Xenix.
[_]	[_]	HD:33	Mod. that corrects artwork errors on the HD interface board.
[_]	[_]	I/O:41	To correct small hash marks when using high res board.
[_]	[_]	I/O:62	Reliability mods for multiterminal board. 4.7k ohm 5% 1/4w res.
[_]	[_]	I/O:101	Prevent DT-100 overrun when connected to multiterminal brd.

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