

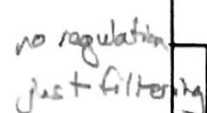
REF DES	TYPE	SPACE INPUT	OUTP
Z5	LS123	1 3	4
Z7	LS27	7 12 11	8
Z1	LM330	6 7	1
Z1	LM329	8 9	14
Z1	LM330	10 11	1

NOTES, UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATION SHOWN: FOR COMPLETE DESIGNATION, ADD WITH PART NUMBER AND OR ASSEMBLY DESIGNATION, AS SHOWN.
2. NUMBERS SHOWN BELOW REFERENCE DESIGNATION IN LOGIC SYMBOLS ARE SEGMENTS OF VENDOR PART NUMBER DESCRIBED IN PARTS LIST.
3. LOGIC SYMBOLS PER USA-STD 732-J4.
4. ELECTRONIC SYMBOLS PER USA-STD 732-57.
5. ALL RESISTOR VALUES ARE IN OHMS 2.2S, 1/4 WATT.
6. ALL CAPACITOR VALUES ARE IN MICROFARADS.
7. ALL DIODES ARE 1N4148.
8. ALL QTD NUMBERED PINS ON J1 ARE GND.
9. HEAT SHOWN FOR REFERENCE ONLY.

VOLTAGE AND GND CHART				
TYPE IC	+5V	+24V	GND	+5V
LS 123	16	-	B	-
MC 3470	11	-	5	-
LM 339	-	3	12	-
MP2 2907	-	-	-	3
462	B	-	4	-
38, 1827, LS01, LS02, LS00, LS01, 07, LS74, LS04, LS08	11	-	7	-

MATERIAL	UNLESS OTHERWISE SPECIFIED			DRAFT	V. TEE	DATE	12-7-81	TITLE	SCHEMATIC DIAGRAM TPI DISKETTE BILLOREY - ER
	TOLERANCES			CHECK		DATE			
	.XX = ±.010 .XXX = ±.005			DESIGN		DATE			
	ANGLES ±1°			APPD		DATE			
	HOLE DIA TOLERANCES			APPD		DATE			
FINISH	014 - .250 = +.005 -.001								
	.251 - .750 = +.008 -.001								
	.751 - UP = +.015 -.001								
	DIMENSIONS ARE IN INCHES AND APPLY AFTER PLATING								
	DO NOT SCALE THIS DRAWING	NEXT ASSY	USED ON					DWG NO	572
			SCALE					SHEET	OF



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AFHRL-70-100

