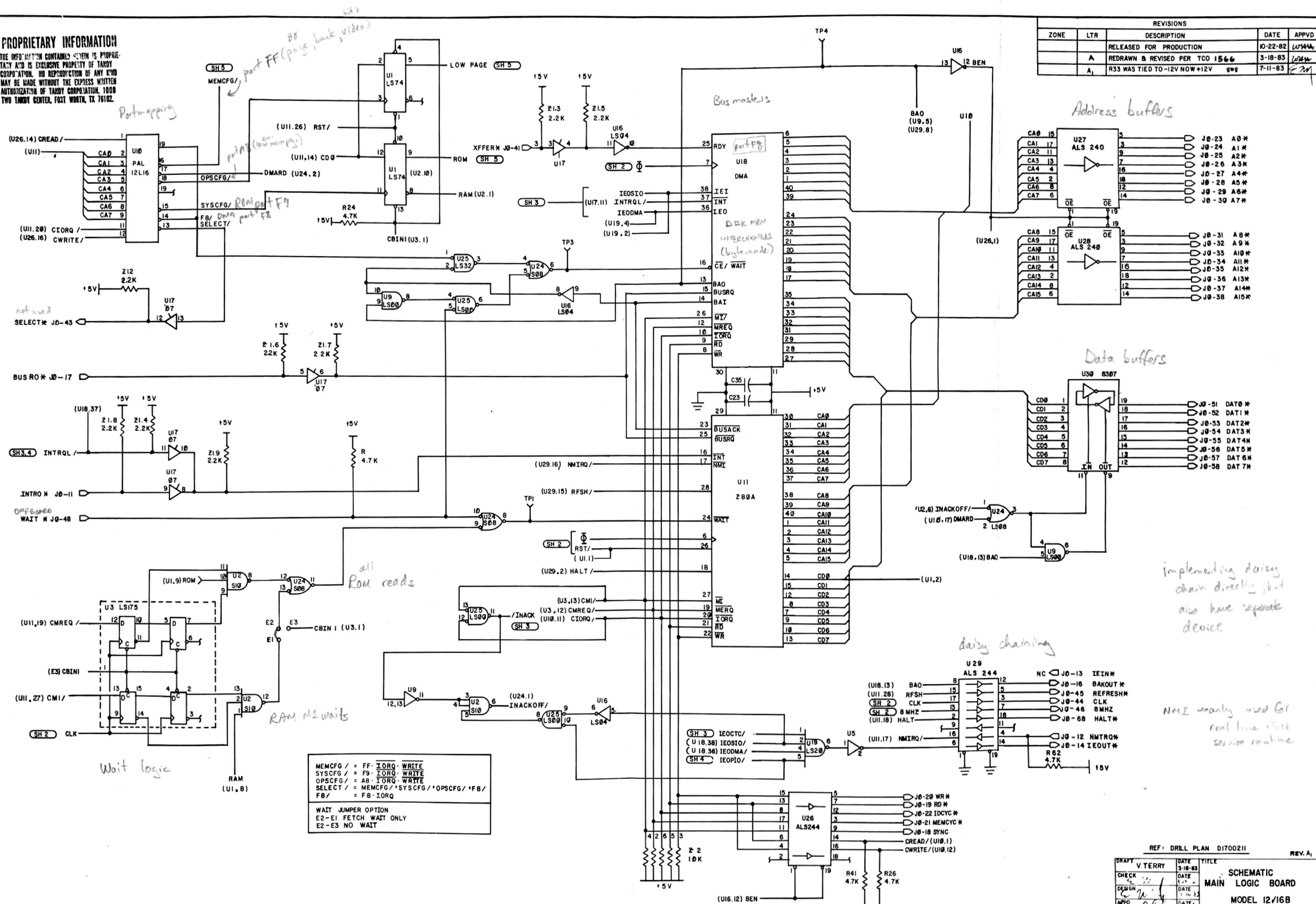


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NMI mainly used for
real time clock
service routine

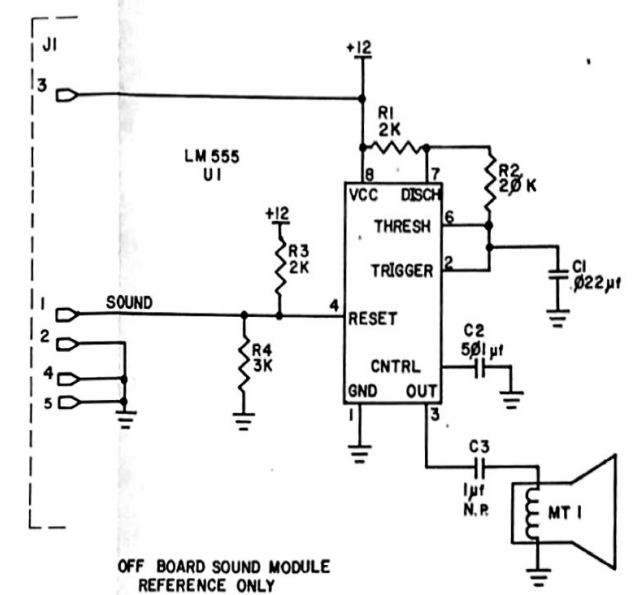
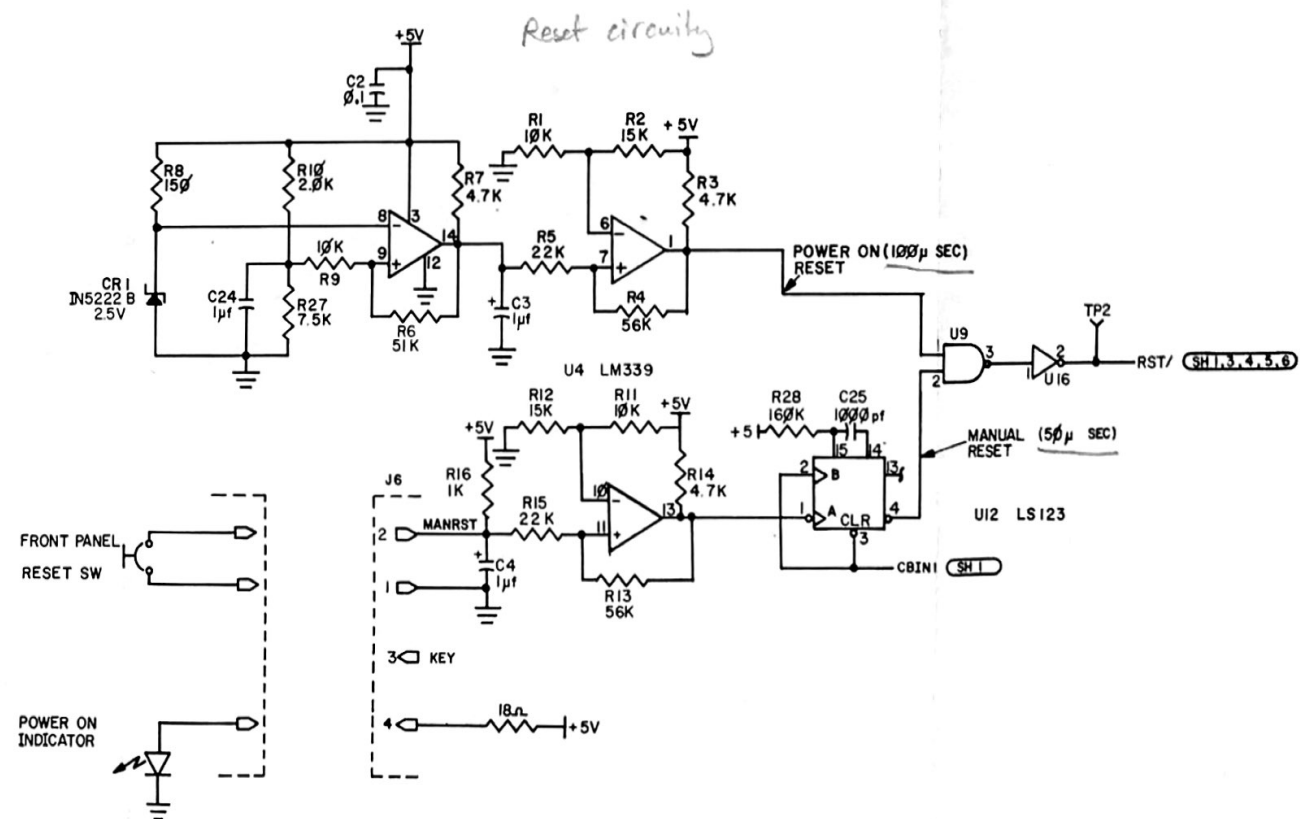
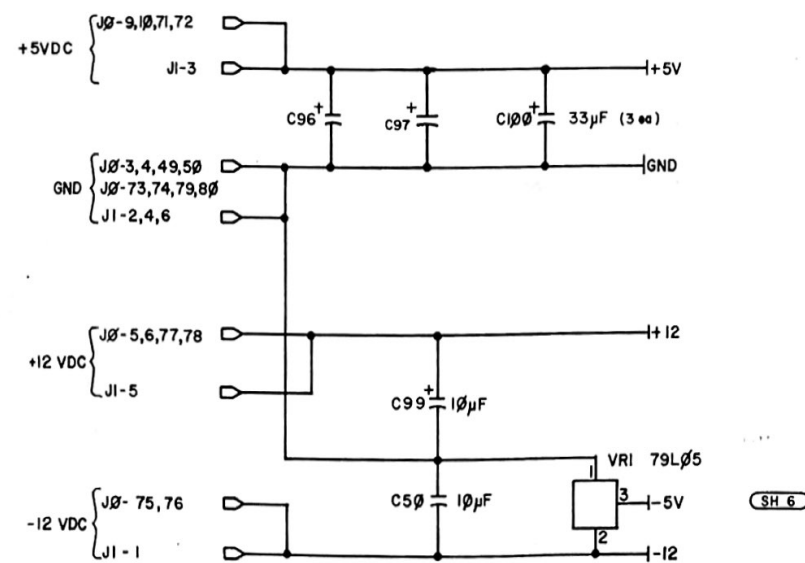
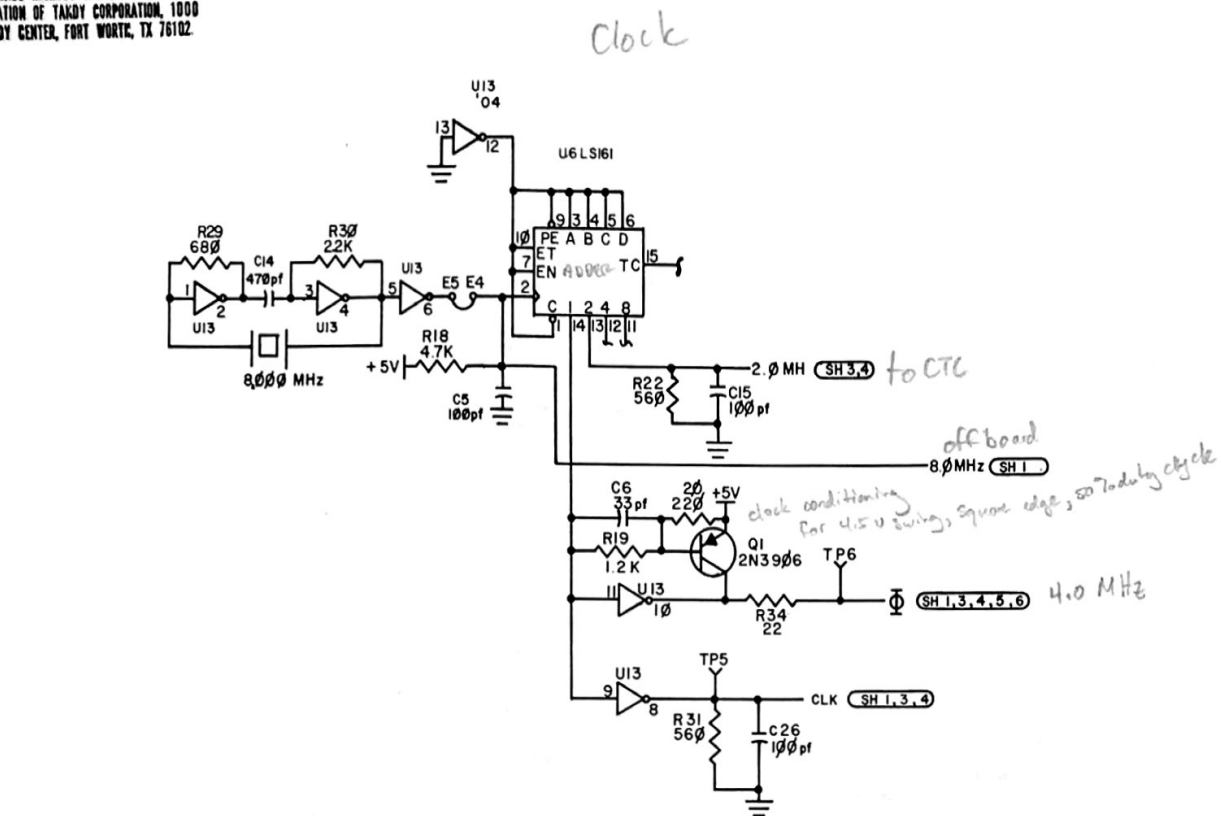
CPU GROUP

DRAFT V TERRY		DATE 3/18/68	TITLE
CHECK		DATE	SCHEMATIC MAIN LOGIC BOARD MODEL 12/16B
DESIGN		DATE 3/18/68	
APPRO		DATE 3/18/68	
APPRO		DATE 3/18/68	
APPRO		DATE 3/18/68	

DWG NO		SIZE
8000166		D
SCALE	SHEET	OF
NONE	1	6

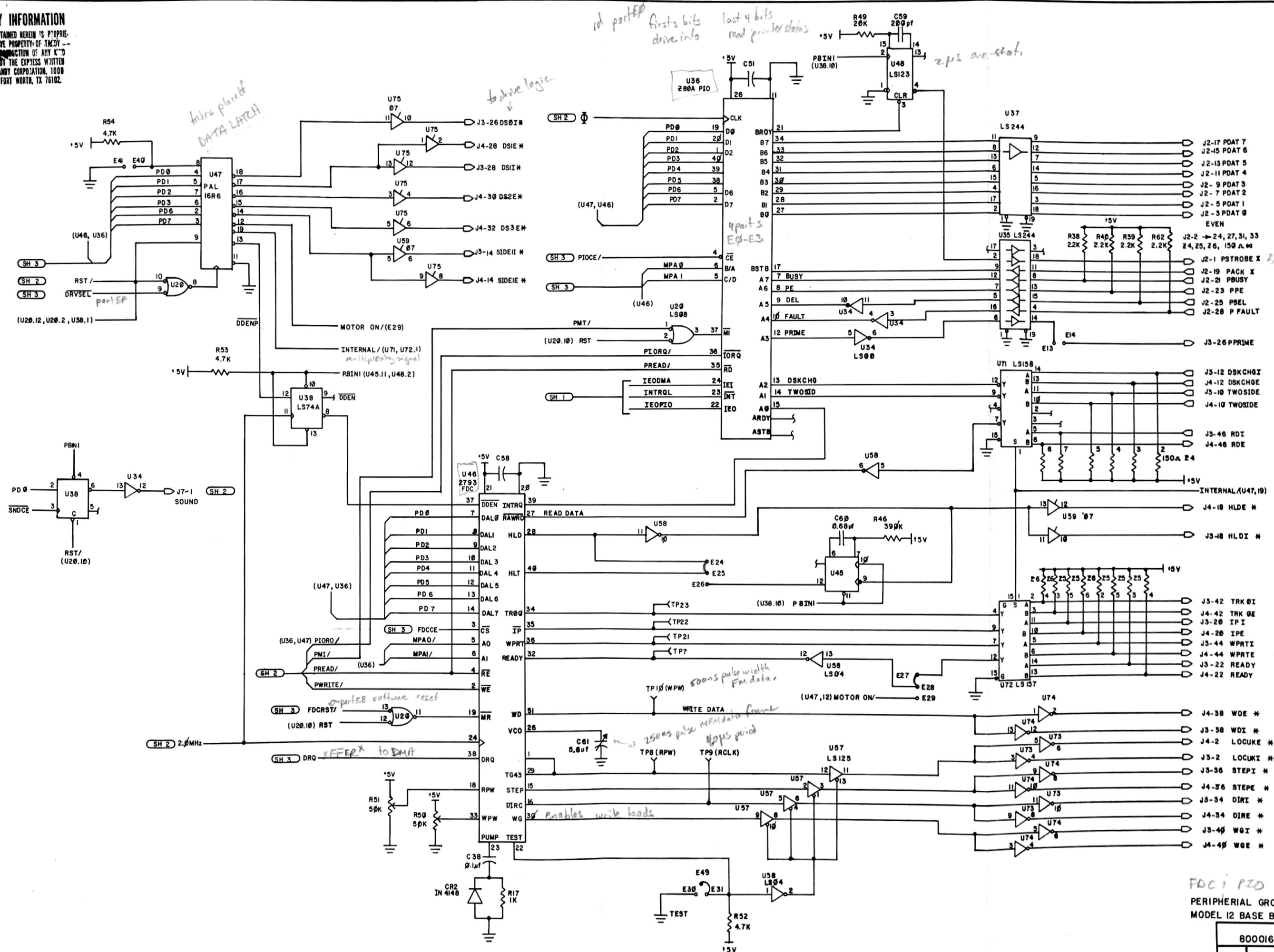
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	PIN NO.	
	NO REV	REV
TAP 30	13	12
TAP 60	3	4
TAP150	11	10

MEMORY GROUP

DWG NO 8000166		REV A ₁
SCALE NONE	SHEET 5 OF 6	

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ZONE	LTR	REVISION	DESCRIPTION	DATE	APPROVED

