

Aug 29 1984 13:09 1984 page 1

From: whom
To: whom
Sent: Wed Aug 29 1984, 11:47:44 CDT
Subject: Serial Status forwarded

==== Start Forwarded Message =====
>From whom Wed Aug 29 11:32:35 1984
To: ron iv bc 4099 bc gordon thol
Sent: Wed Aug 29 1984, 11:32:32 CDT
Subject: Serial Status

Ah, there's bad news tonight. I have come across several things that are causing serial to be the problem it is.

Currently we have a scheme that regulates how often the system is going to interrupt the 68k. The task runs once a second and asks how many characters were received in total during the last second. If less than 16 arrived, it will interrupt the 68k on the arrival of each. If more than 50 arrived, it will interrupt the 68k when 16 have been saved up. If more than 50, it waits until 32 are saved up. All of this is covered by a trap which will flush the queue once a second. This was meant to take care of the case where the alarm level was high and the input rate suddenly dropped to one or two chars per second.

It turns out that this scheme has a built-in flaw. It is a loop sum of all serial traffic. Subsequently, if Z80P or some high speed serial traffic is running, the alarm level would jump to 16 or 50 and the guy in the interactive editor would see bursts of data.

This is really bad in c/cw, where each character you type counts twice. Once as it goes in on the keyboard and once when it comes back to be displayed on your terminal. You only have to type 8 characters in a second to get above the fifo alarm level all by yourself. Pressing <Tab>, which is echoed as several characters, promptly puts the fifo alarm up high.

The solution to this would be to have a separate queue for each input channel. This would give each channel its own alarm level (if needed) and the data would be already broken down by device, which should ease the sorting task on the 68k side.

Another problem has shown itself and explains why c/cu loses so many characters. When cu is running, you will rarely see more than 4 characters in the fifo, even though the alarm level has been set up to a 50 character level. This is because the 68k drains the fifo even when the Z80 did not request that action. The 68k drains the fifo on any reference to the serial hardware. In the case of cu, it receives a character, and then echoes it to the console. When that character is transmitted, the receive queue is emptied again. This defeats the purpose of the alarm mechanism at a time when it is most needed. I am considering "striking" the 68k into thinking there are no characters in the fifo until the Z80 reaches the alarm level or the timeout point. However, this will not solve the problem of fifo alarm-level calculation.

It has also been found that when the console scrolls, we lose one character and sometimes two from the Z80s. This is interesting since the Z80 can buffer three characters on board, giving us just over enough to service

the pending interrupts. The period of time that the screen has interrupts disabled is for about 25 bytes of code, which should be executable in less than 100 usec. Tom has suggested that some other routine is entering screen with interrupts disabled, so that is another area I will check.

Any suggestions or comments on my multi-buffer proposal are welcome.
I suspect that sort of change will be so big that it will get killed by management. Oh well.

Frank

----- End Forwarded Message -----

Charles,

IV (aka John Elliott IV)
Tandy Systems Software, Fort Worth, TX
... Comvax!tvtax, microsoft!trivx@iv

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4) Don't propose it, just do it.
But about it, it will be too little
System a great deal.

- 1) Don't propose it, just end it. The time management gets around to finding out about it, it will be too late to cancel it and we will have improved the system a great deal. This will help the sales of the ioc (we can say).

2) If a multi-buffer scheme will help or increase the throughput of the serial drivers do it. If you need help, on either the z80 or 65k side, I'll be happy to throw in two cents in on off hours (which I sometimes have plenty of). I, for one, would like to see us come out with something that is nice and actually works! I could very soon for the customer that decides to get a 16/12 finds that he can also add a few more channels for his server/taking on users and then discovers that he can only run 250 baud. I realize with our present hardware we are somewhat limited, but it would be nice if our software were capable of 19.2 even though the hardware isn't.

I guess what I'm trying to say is don't waste time talking and discussing changes with management. Most of this was due to management lack of understanding how much work, only to find out later that this did not fit our time frame we suggested took too long or involved too much management, they've replied that it would take too long and/or it wouldn't have been a problem. Most of this was due to management lack of understanding how much work, only to find out later that this did not fit our time frame we suggested over all took too long and/or it wouldn't have been a problem. If you see a way to make serial run faster, send us mail complicated to them.

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From iv Wed Aug 29 13:04:09 1984

To: ron.white@em
Sent: Wed Aug 29 1984, 13:04:09 CDT
Subject: Re: Settling Status
Cc: thui ber roger@ibm.gov

I agree with Ron. Let's do it. If there's anything you'll need in the way of COORD or new XENIX patches let me know. If I have to, I'll do them at home!
See ya later if it kills us... .

TV (aka John Elliott IV)