FROM ROY NEESE & DAVID BUTLER

12Mhz 68000 CPU w 0/wait states modifications and 100ns rams on the memory boards.

68000 CPU Board

1. Remove U1.
2. Insert a socket at U1 (Optional)
3. Insert a 74S32 at U1.
4. Move jumper block from E6-E7 to E5-E7.

68000 Memory Board

1. Remove U9 and all capacitors attached to it.
2. Insert a socket at U9. (Optional)
3. Insert a 74S244 at U9.
4. Install a 10pf capacitor from U9.16 to ground. (U9.19)
5. Install a 10pf capacitor from U9.18 to ground. (U9.19)
6. Cut trace at U9.1 at the feedthrough on the bottom of the memory board.
7. Install a jumper from U9.1 to U9.19.
8. Install a jumper from feedthrough, where you just cut the trace to U10.10.
9. Cut both traces, on the bottom of the memory board, from U10.10.
10. Install a jumper from U10.4 to feedthrough where U10.10 used to be connected.
11. Cut the trace from U13.8.
12. Install a jumper from U13.6 to U11.3.
13. Move block jumper to E12-E14.
14. Install 100ns rams in all of the ram sockets.
15. Install four 74ALS240A's at U5, U6, U7, U8.